



Merlin VME Sales

9432 CONDUCTION COOLED ANALOG OUTPUT MODULE

SPECIFICATIONS

Number of Analog Output Channels: 32
Channel data memory: 16Kx32 deep onboard FIFO
Channel Resolution: 16 bits
Digital Data Format: 16 bits 2's complement or Offset Binary
Resampling Rate: Up to 250 ksa/s. May be supplied on board or from external source
Output Type: Differential
Output Ranges: $\pm 2.5V$
D/A Noise Floor: at least -137dBv per root Hz over 100 kHz bandwidth
Settling time to $\frac{1}{2}$ LSB for 5V step: 4 μ sec

VME Specifications

Meets VME Specifications Revision C.1 IEEE Std.1014-1987
A16:D32 DTB Slave
Address modifier code 29 or 2D HEX
Short I/O Space covering 256 consecutive byte locations, base address configurable within 64K short I/O space
Board size: 6U

Power Requirements

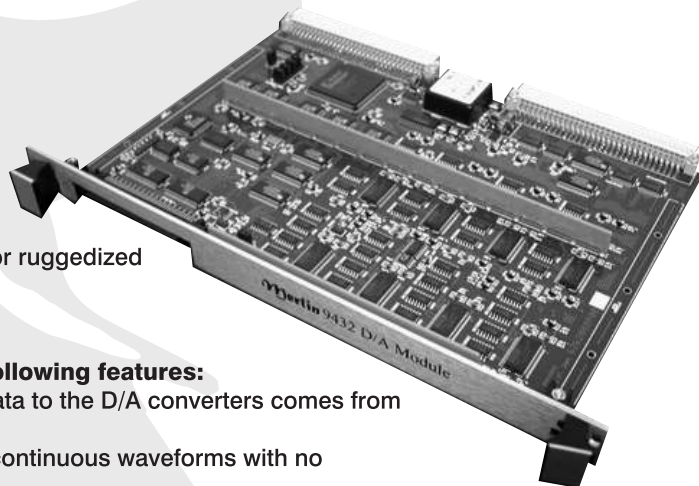
+5 Volts @ 500mA
 ± 12 Volts @ 110ma

Environmental

Operating Temperature: -40 to 85° C air-cooled or ruggedized
Storage Temperature: -40 to 125° C
Shock: 25g, 11ms on all axis

The 9432 Analog Output Module offers the following features:

- The user may program the module so that the data to the D/A converters comes from either the VMEbus or the FPDP interface.
- A Retransmit Mode allows the user to generate continuous waveforms with no processor overhead.
- An onboard A/D converter allows the user to enter a diagnostic mode.
- The auto-diagnostic cycle may be initiated from software or by a system reset. Four data convert cycles are performed on each channel. The results of the diagnostic test are reported in a status register.
- An FPDP test mode allows the user to receive FPDP data which remains stored in the onboard FIFO. The data may then be read from the VMEbus.
- When used with 9531 FPDP RAM Module, unit becomes a 32 channel arbitrary waveform generator.
- Up to 8 boards may be synchronized and share the same FPDP interface. The user writes the ID byte and Channel byte to each board across the VMEbus. To the FPDP interface, multiple boards appear as a single unit.



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