

9531 FRONT PANEL DATA PORT SRAM MODULE

SPECIFICATIONS

Memory Capacity: 8 Mbytes

FPDP Transfer Rate: 60 Mbytes/sec

VME Transfer Rate: 40 Mbytes/sec

VME COMPLIANCE

Meets VME Specifications revision C.1 IEEE Std. 1014-1987

User programmable

A32: D32 DTB Slave

Address modifier code 0B or 09 HEX

Extended I/O space covering 16M consecutive byte locations, base address configurable within 4G I/O Space.

Board size: 6U

POWER SUPPLY REQUIREMENTS

+5 Volts @ 6A

ENVIRONMENTAL

Operating temperature: 0 to 70C

Storage temperature: -25 to 85C

DESCRIPTION

The 9531 FPDP SRAM Module is a VMEbus memory board that may be accessed from either the VMEbus or the bi-directional Front Panel Data Port. The direction of the FPDP interface is set by control registers. The user may also use software to set the number of FPDP transfers, the period of the SYNC pulse, and the speed of the FPDP clock. The PIO[1..2] lines may be set as either inputs or outputs. The module is compatible with any board containing an FPDP interface.

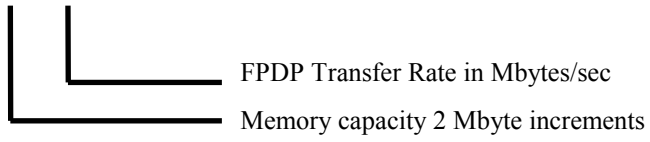
When used with a D/A board the user may construct an arbitrary waveform generator. The waveform data is written to the memory from the VMEbus. The user then configures the DIR bit in the Control Register 1 for FPDP output and writes a logic "1" to the GO bit in Control Register 2. The data may be transmitted continuously or in a single shot from the Front Panel Data Port.

When used with an A/D board the user may collect data. The user configures the DIR bit in Control Register 1 for FPDP input and writes logic "1" to the GO bit in Control Register 2. The user may configure interrupts to be driven when the memory is one quarter full, half full, and full. The user may also configure the board for continuous collection or single cycle collection.

The 9531 may be ordered with up to 8 Mbytes of memory.

ORDERING INFORMATION

M9531 - xx - xx



FPDP Transfer Rate in Mbytes/sec

Memory capacity 2 Mbyte increments